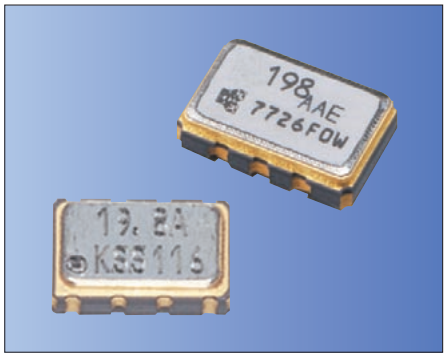
# Temperature Compensated Crystal Oscillators (TCXO)
# Surface Mount Type TCXO (LSI Type) KT5032 Series

KYOCERA

5.0×3.2mm




Pb Free RoHS Compliant

## Features

- Ultra-miniature SMD type (5.0×3.2×1.5mm)
- Reflow compatible
- AFC function available
- 2.3 to 5.5V drive available
- Frequency stability : ±2.0×10⁻⁶/ –30 to +85°C

## Applications

- PDC, GSM, CDMA

## How to Order

KT5032N 26000 D C W 28 T xx
① ② ③ ④ ⑤ ⑥ ⑦ ⑧

① Series
② Output Frequency
③ Frequency Tolerance

| | |
|---|---|
| B | ±1.0×10⁻⁶ |
| C | ±1.5×10⁻⁶ |
| D | ±2.0×10⁻⁶ |

④ Lower Operating Temp.

| | |
|---|---|
| C | –30°C |
| E | –20°C |
| G | –10°C |

⑤ Upper Operating Temp.

| | |
|---|---|
| W | +85°C |
| V | +80°C |
| U | +75°C |

⑥ Supply Voltage

| | | | |
|---|---|---|---|
| 28 | 2.8V | 30 | 3.0V |

⑦ Voltage Control Range

| | |
|---|---|
| TCXO | T |
| VCTCXO | Customer Spec. |

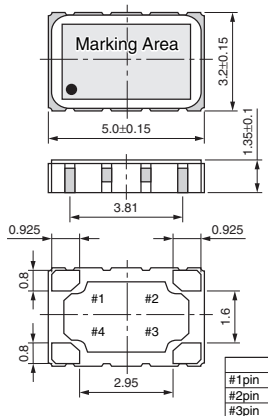
⑧ Option Code

## Specifications

| Item | Symbol | Conditions | Min. | Max. | Units |
|---|---|---|---|---|---|
| Output Frequency Range | fo | Standard Frequency: 13, 19.2, 26, 38.4 | 13 | 40 | MHz |
| Frequency Tolerance | f_tol | vs Temperature | –2 | +2 | ×10⁻⁶ |
| | | vs Load | –0.2 | +0.2 | |
| | | vs Voltage | –0.3 | +0.3 | |
| Frequency Aging | f_age | Per Year | –1 | +1 | ×10⁻⁶ |
| Storage Temperature Range | T_stg | | –40 | +85 | °C |
| Operating Temperature Range | T_use | | –30 | +85 | °C |
| Voltage Control Range | f_cont | Positive | ±8 | ±15 | ×10⁻⁶ |
| Supply Voltage | Vcc | | 2.3 | 5.5 | V |
| Output Level | Vpp | 10k ohm / / 10pF | 0.8 | — | Vp-p |
| Current Consumption | Icc | | — | 2 | mA |
| Harmonics | — | | — | –5 | dBc |

## Dimensions (Unit: mm)

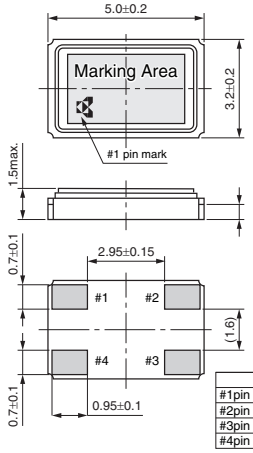
**KT5032N**




| | Pin Configuration |
|---|---|
| #1pin | Vcon |
| #2pin | GND |
| #3pin | OUT |
| #4pin | Vcc |

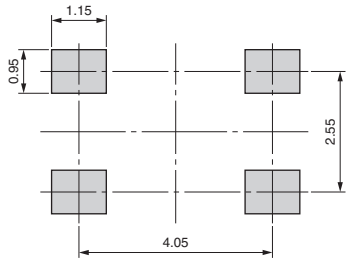
## Dimensions (Unit: mm)

**KT5032P**




| | Pin Configuration |
|---|---|
| #1pin | Vcon |
| #2pin | GND |
| #3pin | OUT |
| #4pin | Vcc |

## Recommended Land Pattern (Unit: mm)




Note: A capacitor of value 0.01μF between VDD and GND is recommended.